

Application Serial Number 10/808,254
Response to Office Action
Dated March 26, 2007

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2. Amendments to the Claims:

A listing of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121. This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) An apparatus comprising: a first output port configured to output a first reference digital test signal; a first input port configured to input a second reference digital test signal; and a second output port configured to output a first stressed digital test signal, wherein the second reference digital test signal is based on the first reference digital test signal and the first stressed digital test signal is based on the second reference test signal.
2. (Currently Amended) The apparatus of claim 1, wherein at least one of the first reference digital test signal, the second reference digital test signal, and the first stressed digital test signal is an optical signal.
3. (Original) The apparatus of claim 1, wherein at least one of the first output port, the first input port, and the second output port is a fiber optic port.
4. (Currently Amended) The apparatus of claim 1, wherein the first reference digital test signal is modulated at an external transmitter to output the second reference digital test signal from the external transmitter.
5. (Currently Amended) The apparatus of claim 122, wherein: the first reference digital test signal and the second reference digital test signal are modulated at different frequencies; and the first reference digital test signal and the second reference digital test signal carry the same data.

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6. (Currently Amended) The apparatus of claim 1, wherein the first reference digital test signal and the second reference digital test signal are modulated at the same wavelength; and the first reference digital test signal and the second reference digital test signal carry substantially the same data.
7. (Currently Amended) The apparatus of claim 4, comprising an internal transmitter, wherein the first reference digital test signal is generated at the internal transmitter.
8. (Currently Amended) The apparatus of claim 1, comprising stressing medium, wherein the second reference digital test signal is stressed at the stressing medium to output the first stressed digital test signal from the stressing medium.
9. (Currently Amended) The apparatus of claim 8, wherein the stressing medium stresses the second reference digital test signal by at least one of: attenuating the second reference digital test signal; creating dispersion in the second reference digital test signal; and creating interference with the second reference digital test signal.
10. (Currently Amended) The apparatus of claim 1, wherein the apparatus monitors at least one of the first reference digital test signal, the second reference digital test signal, and the first stressed digital test signal.
11. (Original) The apparatus of claim 1, wherein: the apparatus comprises a first switch, an internal transmitter, and a stressing medium; the internal transmitter is coupled to the first output port through the first switch, when the first switch is in a first switching state; and the internal transmitter is coupled to the stressing medium through the first switch when the first switch is in a second switching state.
12. (Original) The apparatus of claim 11, wherein the first input port is coupled to the

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stressing medium when the first switch is in the first switching state.

13. (Currently Amended) The apparatus of claim 1, comprising: a second input port configured to input a second stressed digital test signal; a third output port configured to output a third stressed digital test signal; and a third input port configured to input a fourth stressed digital test signal.
14. (Currently Amended) The apparatus of claim 13, wherein the third stressed digital test signal and the fourth stressed digital test signal are the same.
15. (Currently Amended) The apparatus of claim 13, wherein the apparatus monitors at least one of the first reference digital test signal, the second reference digital test signal, the first stressed digital test signal, the second stressed digital test signal, the third stressed digital test signal, and the fourth stressed digital test signal.
16. (Original) The apparatus of claim 13, wherein the second output port is externally coupled to the second input port.
17. (Original) The apparatus of claim 16, wherein the second output port is externally coupled to the second input port by a communication link under test.
18. (Original) The apparatus of claim 17, wherein the communication link under test comprises at least one of: a communication medium; and a communication device.
19. (Original) The apparatus of claim 18, wherein the communication medium is chosen from the group consisting essentially of: optical media; wire line media; and wireless media.
20. (Original) The apparatus of claim 18, wherein the communication device is at least

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one of: an amplifier; a repeater; a coupler; and a polarizer.

21. (Original) The apparatus of claim 13, wherein the third output port is externally coupled to the third input port.
22. (Original) The apparatus of claim 21, wherein the third output port is coupled to an external device under test.
23. (Original) The apparatus of claim 22, wherein the external device under test is an external receiver.
24. (Currently Amended) The apparatus of claim 22, wherein the external device under test inputs the third stressed digital test signal and outputs the fourth stressed digital test signal.
25. (Original) The apparatus of claim 1, wherein the apparatus includes an internal receiver.
26. (Original) The apparatus of claim 13, wherein the apparatus comprises an internal receiver.
27. (Original) The apparatus of claim 26, wherein the internal receiver inputs the fourth stressed test signal.
28. (Currently Amended) A method comprising: outputting a first reference digital test signal from a first output port of a communication testing device; inputting a second reference digital test signal into a first input port of the communication testing device; and outputting a first stressed digital test signal from a second output port of the communication testing device.